



Introduction

Silego's GreenPAK chips have easy to program look-up tables (LUTs) which enable us to easily design a simple yet efficient controller for a line following robot.

Theory

We will explore both the classic approach using only predefined Logic Gates NAND, AND, OR, NOR and NOT, as well as taking full advantage of the programmable LUTs the GreenPAK has.

A line follower is a robot that can follow a track of black lines on a white background, as illustrated below in Figure 1.

Black and white has the most contrast properties and allows us to work with the TCRT5000. The detailed schematic of the TCRT5000 is presented just below in Figure 2.

The TCRT5000 is a Reflective optical sensor, which means that it is composed of two parts: an IR LED and an IR receiver. The output is a digital signal HIGH for white and LOW for Black.

Each TCRT5000 has an output that we are going to call IN 1, IN 2 and IN 3.

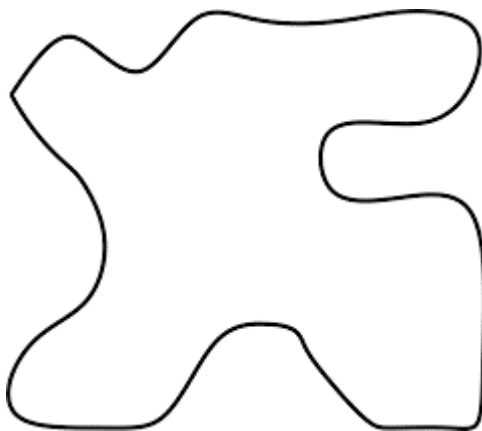


Figure 1. Track of black lines

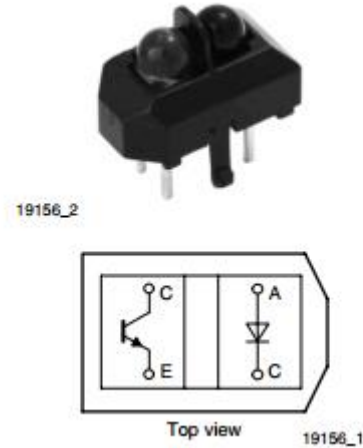


Figure 2. The schematic of the TCRT5000

Design

The logic we are going to be implementing is shown in Table 1:

IN 1	IN 2	IN 3	A MOTOR	B MOTOR
0	0	0	X	X
0	0	1	0	1
0	1	0	X	X
0	1	1	0	1
1	0	0	1	0
1	0	1	1	1
1	1	0	1	0
1	1	1	X	X

Table 1. Logic data



Motor at 1 means that the motor is spinning, and at 0 means that it is stopped. In order to keep a simple design, we will only drive two motors. The motors are placed as shown in Figure 3. We could design the sensor array with any quantity of sensors we like, but it can be just three TCRT 5000 arranged as below. An ideal distance between the TCRT 5000s would be the width of the black line it is going to follow.

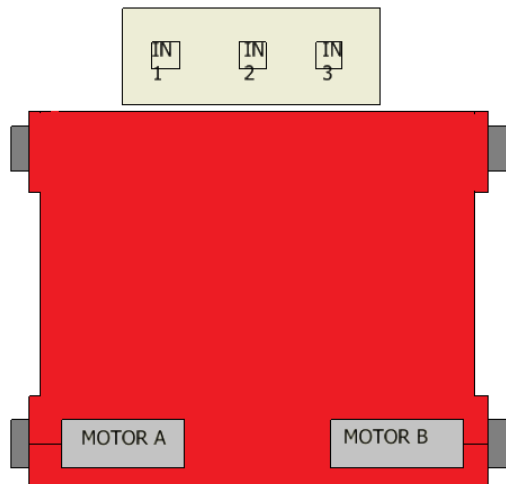


Figure 3. Motor schematic

Classic Design Sequence

Following the normal process using the table shown before, we are to simply obtain the logical equations using Karnaugh's Maps for each motor.

The Karnaugh's map for the A motor is represented in Table 2.

Row IN 1	Columns IN2, IN3	00	01	11	10
0		X	0	0	X
1		1	1	X	1

Table 2. The Karnaugh's map for the A motor

X state means that the combination is not expected to appear under normal circumstances, which means that it does not matter if the motor is either spinning or stopped

So the logic equation for the A motor is:

$$AMotor = IN1$$

The B Motor map is described by Table 3.

Row IN 1	Columns IN2, IN3	00	01	11	10
0		X	1	1	X
1		0	1	0	0

Table 3. The B Motor map

This gives us the logic equation for the B Motor:

$$BMotor = IN1' + IN2'IN3$$

Usually, in order to use only one or two types of logic gates, we do some Boolean algebra until we get the following equivalent equation.

$$BMotor = (IN1(IN2'IN3))'$$

This line follower is developed using simple logic, which means that going in reverse is not implemented.

In case the three sensors are outside the black line, IN 1, IN 2 and IN 3 will all be reading a HIGH, which for this configuration makes the car spin, until the black line can be found again.

On the other hand, when the three sensors are reading a LOW, the car stops moving altogether.

These equations are the logic for the basic line follower, and if we were to program it exactly in the same form, we obtain the configuration shown in Figure 4.

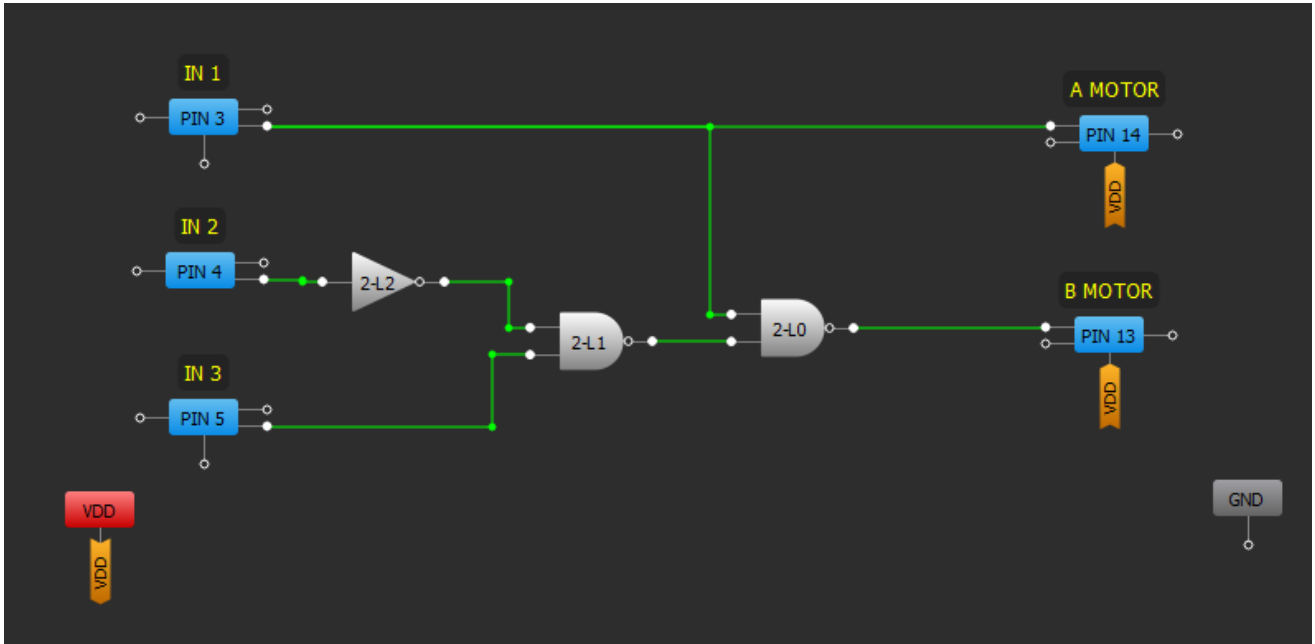


Figure 4. GreenPAK Designer configuration

Using GreenPAK for Design Optimization

GreenPAK has the advantage that the designs can be implemented using premade logic gates. But what makes it so favored by engineers is their capability to actually implement completely arbitrary Logic tables using their programmable LUTs.

Figure 5 illustrates that using the LUT table is far easier than implementing predetermined logic gates

Using the completely programmable feature of the LUT we can considerably reduce the use of resources inside the GreenPAK, and simplify even further the Line follower design.

In Figure 6 we can see that just one LUT table can do all the processing done by the other 3 Logic Gates shown in the first GreenPAK Design.

Properties

3-bit LUT1/DFF/LATCH4

Type: LUT

IN3	IN2	IN1	IN0	OUT
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

Standard gates: Regular shape

Defined by user

All to 0, All to 1, Invert

Apply

Figure 5. LUT properties

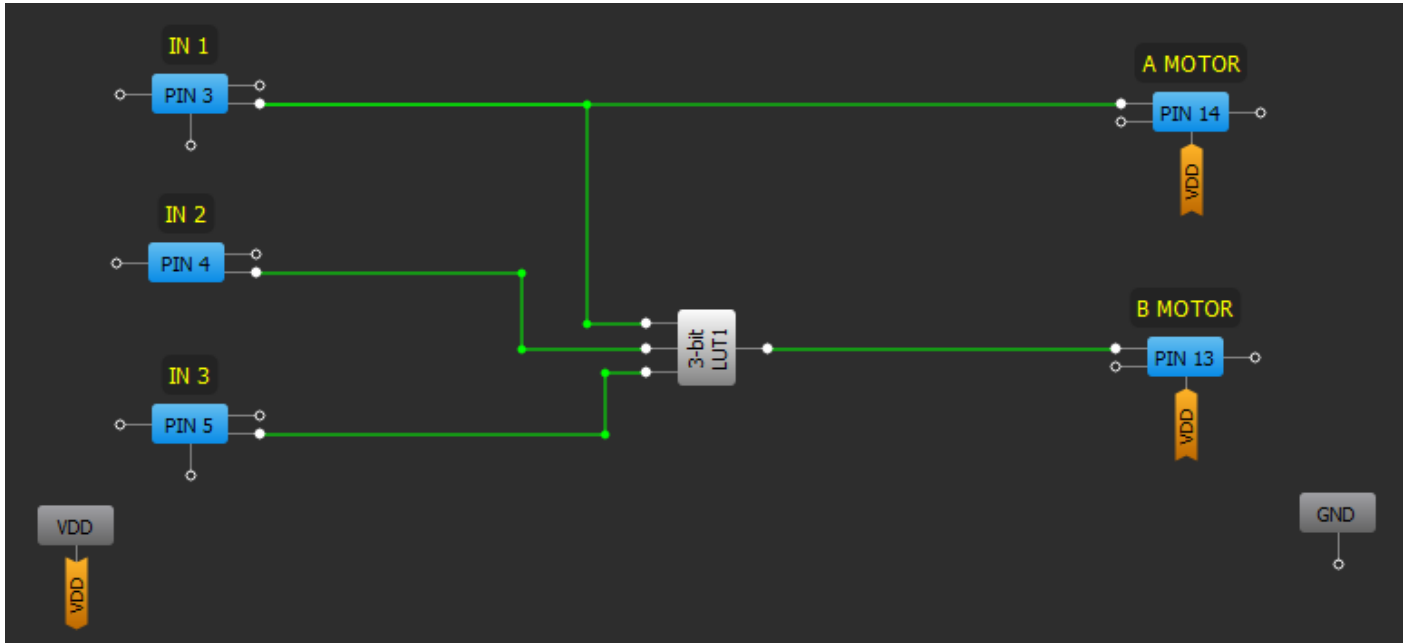


Figure 6. 3 Logic Gates in GreenPAK Design

Considerations

To power up the GreenPAK, we can connect it to the 5v Pin of the LN298N and power the same using a 12 volt battery. This can be seen in Figure 7.

Using this setup, we can add more features to this basic design, implementing PWM to regulate the speed in which the line follower is moving, and enable us to implement a reverse condition if needed.

Conclusion

This app note shows a simple design that can always be modified to add new features.

GreenPAKs are a very promising tool for IoT development and as a tool for teaching future electronic engineers. Their low power consumption, small design and user friendly developing software makes them an interesting and handy technology to become mainstream in study plans for colleges around the world.

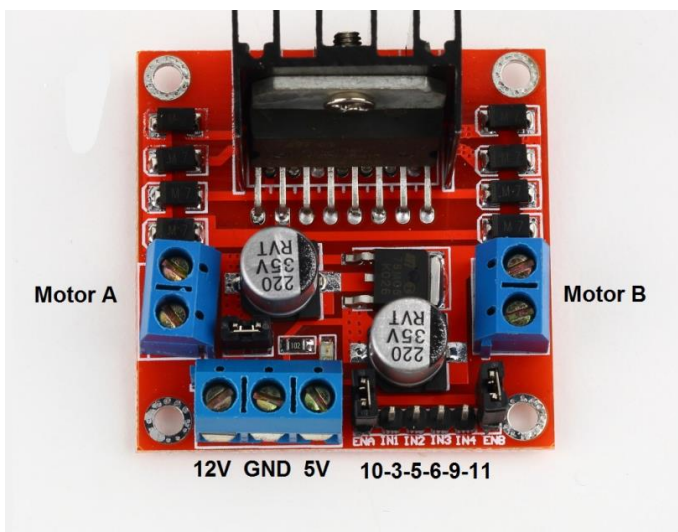


Figure 7. The LN298N



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A	Onésimo Ivan Fernandez	11/04/2016	New application note

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