

1 Install CurrentPAK Designer Software

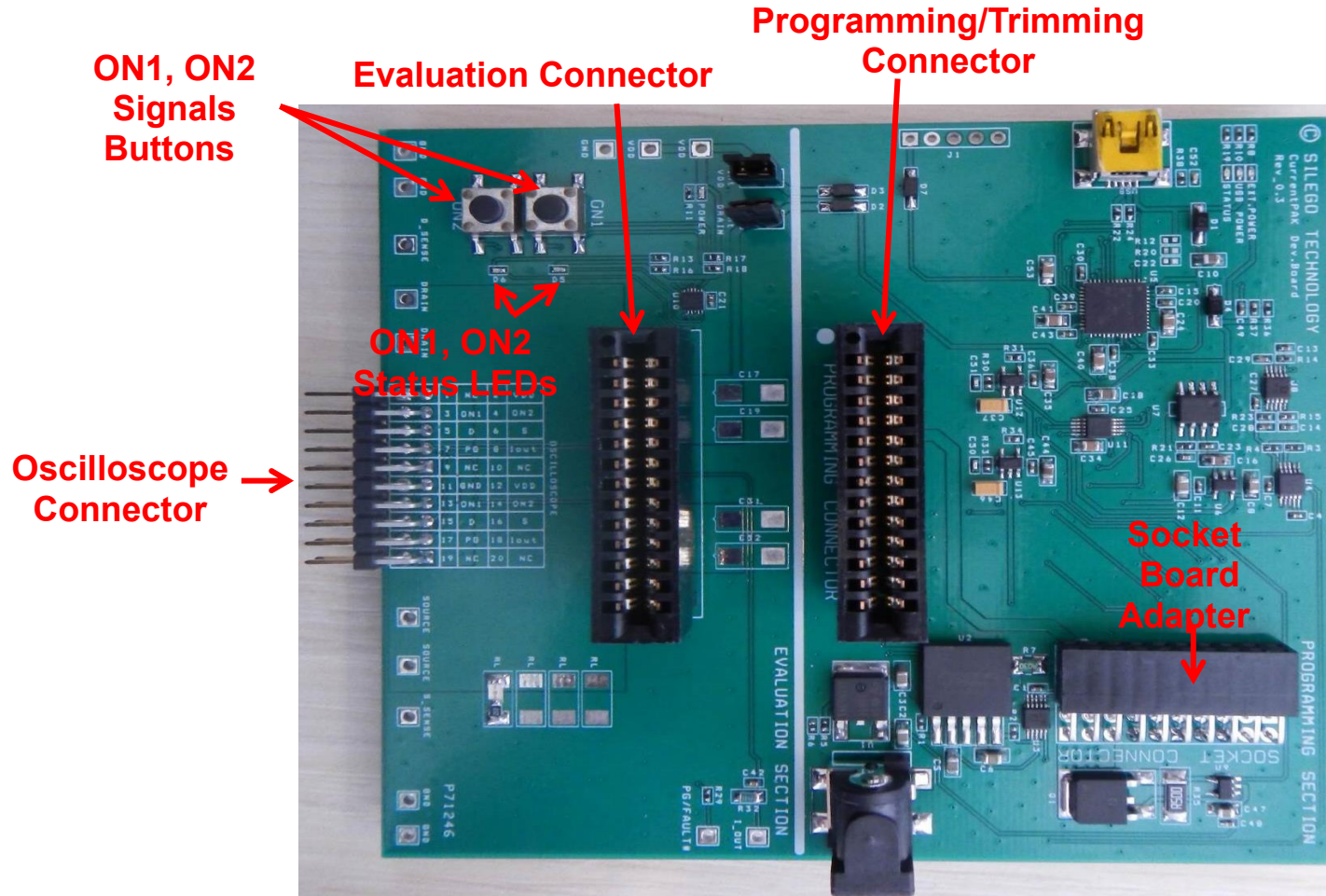
Download and install the latest CurrentPAK Designer software. CurrentPAK Designer development software is an easy-to-use full-featured integrated development environment that allows you to configure the chip.

2 Configure available options for your own project

Start CurrentPAK Designer Software which provides a direct access to all CurrentPAK device features and complete control over configuration options. CurrentPAK Designer has simple and intuitive software interface to CurrentPAK Development Board that gives you a quick and easy way to program and evaluate your entire CurrentPAK project with just one tool.

3 Prepare Development Board

Use the included USB cable to connect CurrentPAK Development Board to your Windows or MacOS based computer. Make sure that the socket board is well connected to main board.



4 Use CurrentPAK Development Board

Working in tandem with the CurrentPAK Designer, Silego Technology’s CurrentPAK Development Board is divided into 2 sections:

- **Programming** section used for read/trim/program operations
- **Evaluation** section used for testing of programmed CurrentPAK samples

5 Use CurrentPAK Programmer Tool

CurrentPAK Programmer Tool provides possibility to program CurrentPAK samples in a few minutes.

Using the included CurrentPAK Programmer Tool with CurrentPAK Development Board allows:

- Read and Program CurrentPAK samples with Programming section of CurrentPAK Development Board;
- Change Trim parameters in Advanced mode (All parameters by default have optimal value for chip functionality);
- Test programmed configuration on Evaluation section of CurrentPAK Development Board using buttons ON1/ON2 or software buttons emulation;

(Note 1): Do not clear bits spare bits [21:63]. If these bits are cleared, close and re-open CurrentPAK Designer to restore to default.

